

IN THE CLAIMS:

Please amend claim 1, and add a new claim 29 as follows:

1. (Currently Amended) An image display device, comprising:
 - a display part configured by a plurality of pixels each having an electro-luminescent element driven to illuminate according to a display signal voltage V_s ;
 - a signal line used to write said display signal voltage in said pixel;
 - a pixel selector for selecting a pixel from said plurality of pixels so as to write said display signal voltage therein through said signal line;
 - a display signal voltage generator for generating said display signal voltage;
 - an illuminating state controller for controlling a selection of an illuminating state or a non-illuminating state for each of said plurality of pixels at a time; and
 - a constant voltage supply for supplying a constant voltage V_{il} to each of said plurality of pixels through said signal line when said illuminating state is selected for said selected pixel,

wherein one end of said electro-luminescent element provided in each said pixel is connected to a common power supply while the other end of said electro-luminescent element is selectively connected to a first source/drain electrode of an electro-luminescent element driving transistor through a first switch, said transistor has a threshold voltage V_{th} ,

a second source/drain electrode of said electro-luminescent element driving transistor is connected to a power supply line applied with said common supply voltage, and

the gate of said electro-luminescent element driving transistor is connected to the signal line through a capacitance and selectively connected to the first source/drain electrode of said electro-luminescent element driving transistor through a second switch, and

when said illuminating state is selected, the first switch is fixed as ON, the second switch is fixed as OFF, and the constant voltage is applied to the signal line such that a voltage that is lower than said common supply voltage appears at the gate of said transistor.
2. (Previously Presented) The image display device according to claim 1;

wherein the gate of said electro-luminescent element driving transistor is connected to said signal line corresponding to each pixel through a connection capacitor.

3. (Original) The image display device according to claim 2; wherein said first source/drain electrode is a drain electrode and said second source/drain electrode is a source electrode.
4. (Original) The image display device according to claim 2; wherein each of said first switch, said second switch, and said electro-luminescent element driving transistor is a p-channel transistor.
5. (Original) The image display device according to claim 2; wherein each of said first switch, said second switch, and said electro-luminescent element driving transistor is configured as a p-channel transistor and said connection capacitor is a MOS (Metal-Oxide-Semiconductor) capacitor that uses a p-channel.
6. (Original) The image display device according to claim 2; wherein each of said first switch, said second switch, and said electro-luminescent element driving transistor is a polycrystalline silicon thin film transistor.
7. (Original) The image display device according to claim 2; wherein each of said first switch, said second switch, and said electro-luminescent element driving transistor is an n-channel transistor.
8. (Original) The image display device according to claim 2; wherein each of said first switch, said second switch, and said electro-luminescent element driving transistor is an n-channel transistor and said connection capacitor is a MOS (Metal-Oxide-Transistor) capacitor that uses an n-channel.
9. (Previously Presented) The image display device according to claim 2; wherein each of said first switch, said second switch, and said electro-luminescent element driving transistor is an amorphous silicon thin film transistor.

10. (Original) The image display device according to claim 2; wherein said signal line and said power supply line are disposed in parallel and formed by processing the same metallic wiring layer.
11. (Original) The image display device according to claim 10; wherein said connection capacitor is provided on said signal line in layers.
12. (Original) The image display device according to claim 2; wherein said electro-luminescent element driving transistor is actually driven in a sub-threshold area in which its gate-source voltage is a threshold voltage and under.
13. (Previously Presented) The image display device according to claim 1;
wherein one end of the signal line is connected to the display signal voltage generator through a third switch.
14. (Original) The image display device according to claim 13; wherein said first source/drain electrode is a drain electrode and said second source/drain electrode is a source electrode.
15. (Previously Presented) The image display device according to claim 13; wherein each of said second switch and said electro-luminescent element driving transistor is a p-channel transistor.
16. (Previously Presented) The image display device according to claim 13; wherein each of said second switch and said electro-luminescent element driving transistor is a p-channel transistor and said connection capacitor is configured by a MOS (Metal-Oxide-Transistor) capacitor that uses a p-channel.
17. (Previously Presented) The image display device according to claim 13; wherein each of said second switch and said electro-luminescent element driving transistor is a polycrystalline silicon thin film transistor.

18. (Previously Presented) The image display device according to claim 13; wherein each of said second switch and said electro-luminescent element driving transistor is an n-channel transistor.
19. (Previously Presented) The image display device according to claim 13; wherein each of said second switch and said electro-luminescent element driving transistor is configured as an n-channel transistor and said connection capacitor is configured by a MOS (Metal-Oxide-Semiconductor) capacitor that uses an n-channel.
20. (Previously Presented) The image display device according to claim 13; wherein each of said second switch and said electro-luminescent element driving transistor is configured by an amorphous silicon thin film transistor.
21. (Original) The image display device according to claim 13; wherein said signal line and said power supply line are disposed vertically to each other and said power supply line is formed by processing a metallic wiring layer.
22. (Original) The image display device according to claim 21; wherein said connection capacitor is formed on said signal line in layers.
23. (Original) The image display device according to claim 13; wherein said electro-luminescent element driving transistor is actually driven in a sub-threshold region in which its gate-source voltage is a threshold voltage and under.
24. (Original) The image display device according to claim 1; wherein selection of said illuminating/not-illuminating state is repeated in each frame period.
- 25-28. (Cancelled)
29. (New) The image display device according to claim 1, wherein said voltage appearing at the gate of said transistor is lower than said common supply voltage by $(V_s - V_{il} + |V_{th}|)$.